Weekly Progress Report

T12

LUIS SANTIAGO

Week ending 04/05/2015

**Completed Tasks:**

1. Came up with LFSR implementation that generates all 32 possible values.
2. Created the schematic for LFSR
3. Re-did schematic on eagle defaults values ( for uniformity of all experiments)
4. Integrated clock and power to ISI experiment

**Tasks to Be Completed Next Week:**

1. Review schematic with Dr. Morris and Tony
2. Write detailed experiment read
3. Route experiment board
4. Order new parts needed to implement new LFSR design

**Issues:**

Previous problem solved by using NXOR instead to avoid the need to program a seed and changed some of the traces to use 3 bit instead of all four bits.